BAKER BOTTS LLP

PETITION FOR EXTENSION OF TIME UNDER 37 CFR 1.136(a)			Docket Number (Optional) A33828 PCT USA		
OIPE	In re Application of Fuh	Fuhr			
NEC 0 8 2004	Application Number 09/720,275		Filed December 21, 200		
1 ()(20 - 14)	For ELECTRODE ARRANGEMENTS FOR				
RADEMINIST.	Group Art Unit	t Unit Examiner Mutschler, Brian L.			
This is a request under the provisions of reply in the above identified application.	37 CFR 1.136(a) to extend the period fo	r filing a			
The requested extension and appropriat (check time period desired):	e non-small-entity fee are as follows				
X One month (37 CFR 1.17(a)	(1)) \$110		\$ <u>110</u>		
Two months (37 CFR 1.17(a	a)(2))		\$		
Three months (37 CFR 1.17(a)(3))		\$			
Four months (37 CFR 1.17(a)(4))			\$		
Five months (37 CFR 1.17(a)(5))			\$		
above is reduced by one-half, and A check in the amount of the fee i		ee amoui	nt snown		
Payment by credit card. Form PTO-2038 is attached.					
The Commissioner has already been authorized to charge fees in this application to a Deposit Account.					
The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment, to Deposit Account Number 02-4377 I have enclosed a duplicate copy of this sheet.					
I am the applicant/inventor			:		
assignee of record of the entire interest. See 37 CFR 3.71. Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96).					
x attorney or agent of record.					
attorney or agent unde Registration number if a	r 37 CFR 1.34(a). octing under 37 CFR 1.34(a)	\wedge			
WARNING: Information on this for be included on this form. Provide	rm may become public. Credit card information and authoriza	or dation tigh on F	should not		
12-6-2004	the standard	193/			
Date	Signa	ture			
PTO Reg No.: 27,551	Bradley E	. Geis	t		
		or printe			
NOTE: Signatures of all the inventors or assignees forms if more than one signature is required, see by		ve(s) are re	equired. Submit multiple		
Total offorms are submitted	i.				

BAKER BOTTS LLP

Attorney Docket Number: A33828 PCT USA

T	it	le	:

ELECTRODE ARRANGEMENTS FOR GENERATING FUNCTIONAL FIELD BARRIERS IN

MICROSYSTEMS

Use Space Below for Additional Information: